

<b>Notice of Allowability</b>	Application No.	Applicant(s)	
	10/020,558	DIJAILI ET AL.	
	Examiner	Art Unit	
	Phillip Nguyen	2828	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to 8/11/04.
2.  The allowed claim(s) is/are 1,5-12,14-16 and 31-42.
3.  The drawings filed on 12/14/01 are accepted by the Examiner.
4.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a)  All
  - b)  Some\*
  - c)  None
 of the:
  1.  Certified copies of the priority documents have been received.
  2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
  - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftsperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 4/5-5/6/02,8/11/04
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.

**DETAILED ACTION**

***Allowable Subject Matter***

The following is an examiner's statement of reasons for allowance: Claims 1, 5-12, 14-16 and 31-42 are allowed because the prior art fail to teach or fairly suggest an optical logic gate comprising a first lasing semiconductor optical amplifier having an amplifier input, amplifier output, a pump, and a first ballast laser output wherein the first ballast laser output coupled to a digital output for outputing an optcal digital output signal.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Communication Information***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phillip Nguyen whose telephone number is 571-272-1947. The examiner can normally be reached on 9:00 AM - 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, MINSUM HARVEY, can be reached on 571-272-1835. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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### **EXAMINER'S AMENDMENT**

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Since the applicant elected species of group II for examination without traverse, the non-elected claims belonged to other groups are now canceled.

The application has been as amended as followings:

In the claims:

Claims 2-4 and 17-30 are canceled.

### ***Information Disclosure Statement***

The missing PTO-1449 submitted on 7/5/2002, was resubmitted on 8/11/2004, has been considered along with IDSs submitted on 4/05/02 and 5/06/02.

***Allowable Subject Matter***

The following is an examiner's statement of reasons for allowance: Claims 1, 5-12, 14-16 and 31-42 are allowed because the prior art fail to teach or fairly suggest an optical logic gate comprising a first lasing semiconductor optical amplifier having an amplifier input, amplifier output, a pump, and a first ballast laser output **wherein the first ballast laser output coupled to a digital output for outputting an optcal digital output signal.** Song ('112) teaches a logic gate configured from semiconductor optical amplifiers (SOAs) in Figures 1, 3-4, and 8 having digital inputs coupled to the SOA inputs, digital output coupled to the SOA output. However, Song does not teach using LSOA to configure a digital optical logic gate with a ballast laser having a ballast laser output coupled to the digital output of the logic gate.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Communication Information***

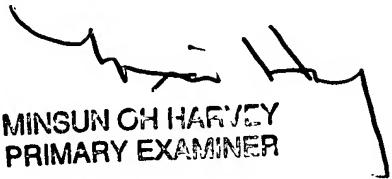
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MINSUN OH HARVEY  
PRIMARY EXAMINER